

EAST - [Untitled1.1]
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L3: (32) (wait adj signal) and 710/260-269.cccls.
L4: (127) (wait\$2 adj signal) and 710/104-125.cccls.
L5: (80) (wait\$3 same system same (freez\$3 or frozen)).bsum.
L6: (4) (wait\$3 same system same bus same (freez\$3 or frozen)).bsum.
L7: (97) wait\$3 same (pc adj card)
L8: (2550) wait\$3 same signal\$4 same insert\$3
L9: (88) 18 same interrupt\$3 same (cpu or processor)
L10: (33) 18 and 710/300-304.cccls.
L11: (99) (wait\$3 adj2 signal\$4) same (interrupt\$3 near2 (cpu or processor))
L12: (94) (wait\$3 adj signal\$4) and (interrupt\$3 near2 (cpu or processor)) and ((insert\$3 or rem...
L13: (39) wait same smi same (insert\$3 or remov\$3).bsum.
L14: (0) (wait\$2 adj signal) same smi
L15: (129) (wait\$3 same system same bus same (freez\$3 or frozen or idle)).bsum.
L16: (5) (pc adj card) same (live or on-line) same (insert\$3 or remov\$3)
L17: (25) (wait adj state\$3) same smi
L18: (16) 710/300-304.cccls. and 710/260-269.cccls.
L22: (6) (idle or freez\$3 or frozen) same 121
L21: (156) (wait adj state\$3) same (interrupt\$3 near2 (cpu or processor))
L23: (62) (wait adj state\$3) and 710/300-304.cccls.
L24: (59) ((hold or wait) adj signal) and 710/260-269.cccls.
L25: (168) wait\$3 and phan.xa. and 710/5.cccls.
L26: (46) (wait\$3 adj (state\$3 or signal\$3)) and phan.xa. and 710/5.cccls.
L27: (46) (wait\$3 same system same bus same contention).bsum.
L28: (26) (bus adj contention) and 710/300-304.cccls.
L29: (33) (bus adj contention) same (wait adj (state\$3 or signal))
L30: (63) smi and 710/300-304.cccls.
L31: (27) smi and wait and 710/300-304.cccls.
L32: (33) smi and (wait or hold) and 710/300-304.cccls.
L33: (33) smi and (wait or hold) and 710/300-304.cccls.
L34: (300) (wait\$2 adj signal) same interrupt\$3
L35: (48) (wait\$2 adj signal) same (interrupt\$3 near2 (cpu or processor))
L36: (13) (wait\$2 adj signal) same (pc adj card)
L37: (21538) (interrupt\$3 near2 (cpu or processor))
L39: (5) card\$2 same 138
L38: (194) (wait\$2 adj (state\$2 or signal)) same (interrupt\$3 near2 (cpu or processor))
L40: (0) card\$2 same (wait\$3 or hold\$2) same signal same interrupt\$3 same (cpu or processor)
L41: (115) card\$2 same (wait\$3 or hold\$2) same signal same interrupt\$3 same (cpu or processor)
L46: (0) interrupt\$3 and 6477596.pn.
L47: (2698) interrupt\$3 same wait\$3 same timer
L48: (211) interrupt\$3 same (wait\$3 adj timer)
L49: (15) (interrupt\$3 near2 (cpu or processor)) same (wait\$3 adj timer)
L50: (4) 710/5.cccls. and 148
L51: (130) (interrupt\$3 near2 (cpu or processor)) and 710/300-304.cccls.
L52: (237) smi same timer
L53: (12) 152 same insert\$3
L54: (21) 152 same card\$3
L55: (118) smi same insert\$3
L56: (24) card\$2 same 155
L57: (0) ((pc adj card) same freez\$3).bsum.
L58: (4) ((pc adj card) same idle).bsum.
L59: (0) (negat\$3 same (wait adj signal) same longer same time).bsum.
L60: (4) (negat\$3 same (wait adj signal) same time).bsum.
L61: (16) (negat\$3 same (wait adj signal)).bsum.
L62: (37) (assert\$3 same (wait adj signal)).bsum.
L63: (2) (((keep\$3 or kept) near3 assert\$3) same (wait adj signal)).bsum.
L64: (10) (((keep\$3 or kept) near3 assert\$3) same (wait adj signal)).

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L25: (168) waits\$3 and phan.xe. and 710/\$.ccls.
L26: (46) (waits\$3 adj (state\$3 or signal\$3)) and phan.xe. and 710/\$.ccls.
L27: (46) (waits\$3 same system sema bus same contention).bsum.
L28: (26) (bus adj contention) and 710/300-304.cccls.
L29: (33) (bus adj contention) same (wait adj (state\$3 or signal))
L30: (63) smi and 710/300-304.cccls.
L31: (27) smi and wait and 710/300-304.cccls.
L32: (33) smi and (wait or hold) and 710/300-304.cccls.
L33: (33) smi and (wait or hold) and 710/300-304.cccls.
L34: (300) (waits\$2 adj signal) same interrupt\$3
L35: (48) (waits\$2 adj signal) same (interrupt\$3 near2 (cpu or processor))
L36: (13) (waits\$2 adj signal) same (pc adj card)
L37: (21538) (interrupt\$3 near2 (cpu or processor))
L39: (5) card\$2 same 138
L38: (194) (waits\$2 adj (state\$2 or signal)) same (interrupt\$3 near2 (cpu or processor))
L40: (0) card\$2 same (waits\$3 or holds\$2) same signal same interrupt\$3 same (cpu or processor)
L41: (115) card\$2 same (waits\$3 or holds\$2) same signal same interrupt\$3 same (cpu or processor)
L46: (0) interrupt\$3 and 6477596.PN.
L47: (2698) interrupt\$3 same waits\$3 same timer
L48: (211) interrupt\$3 same (waits\$3 adj timer)
L49: (15) (interrupt\$3 near2 (cpu or processor)) same (waits\$3 adj timer)
L50: (4) 710/\$.ccls. and 148
L51: (130) (interrupt\$3 near2 (cpu or processor)) and 710/300-304.cccls.
L52: (237) smi same timer
L53: (12) 152 same insert\$3
L54: (21) 152 same card\$3
L55: (118) smi same insert\$3
L56: (24) card\$2 same 155
L57: (0) ((pc adj card) same freez\$3).bsum.
L58: (4) ((pc adj card) same idle).bsum.
L59: (0) (negat\$3 same (wait adj signal) same longer same time).bsum.
L60: (4) (negat\$3 same (wait adj signal) same time).bsum.
L61: (16) (negat\$3 same (wait adj signal)).bsum.
L62: (37) (assert\$3 same (wait adj signal)).bsum.
L63: (2) (((keep\$3 or kept) near3 assert\$3) same (wait adj signal)).bsum.
L64: (10) (((keep\$3 or kept) near3 assert\$3) same (wait adj signal))
L65: (0) ((keep\$3 or kept) near3 assert\$3) same (wait) same prevent\$3
L66: (2) ((keep\$3 or kept) near3 assert\$3) same (wait) same prevent\$3
L67: (1675) (waits\$3 same (long adj time)).bsum.
L68: (3) (waits\$3 same (long adj time) same assert\$3).bsum.
L69: (196) (waits\$3 same time same assert\$3).bsum.
L70: (114) (waits\$3 same time same bus same assert\$3).bsum.
L71: (45) ((waits\$3 adj (signal or state\$2 or period\$2)) same time same bus same assert\$3).bsum.
L72: (151) ((waits\$3 or holds\$3) same time same bus same assert\$3).bsum.
L73: (21) ((hold\$3 near3 bus) same interrupt\$3 same (cpu or processor)).bsum.
L78: (2) (bus near3 (freez\$3 or frozen)) same (interrupt\$3 near2 (cpu or processor))
L79: (24) (bus adj2 idle) same (interrupt\$3 near2 (cpu or processor))
L80: (112) (wait adj signal) same timer
L81: (1) "6020931".PN.
L82: (1) "5797018".PN.
L83: (1) "5469561".PN.
L84: (1) "5444857".PN.
L85: (1) "5278974".PN.
L86: (1) "4901226".PN.
L87: (1) "4851990".PN.
L88: (116) (wait adj signal) and 714/\$.ccls.

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